



- CALIBRATION PROCEDURE**
- CONNECT A 4 OHM LOAD RESISTOR TO PINS 1 & 4 AND SHORT PINS 2 & 3 OF CONNECTOR P₃ (X, Y).
 - OUTPUT STAGE BIAS CURRENT ADJUSTMENT. (THE BIAS CAN BE ADJUSTED BY REMOVING THE HEAD CABINET BAFFLE. USE A LONG ROD AND TAP THE BAFFLE FREE FROM THE HEADLOCK RETAINERS. GAIN ACCESS FROM THE REAR OF THE CABINET BY USING THE SPACE AVAILABLE THROUGH THE COOLING FAN BLADES.)
 - ADJUST VR₁ FOR ±0.72 VOLTS D.C. BETWEEN K₁ AND GROUND.
 - ADJUST VR₂ FOR ZERO 2.01 VOLTS BETWEEN K₁ AND K₂.
 - PHASE INVERTER BALANCE CONTROL ADJUSTMENT.
 - HARMONIC DISTORTION METER METHOD. DRIVE AMPLIFIER TO 25 VOLTS R.M.S. OUT AT 40 HZ AND CONNECT DISTORTION METER TO LOAD RESISTOR. ADJUST VR₃ FOR MINIMUM DISTORTION.
 - VOLT METER METHOD. ADJUST OUTPUT AS IN STEP 3-A AND CONNECT D.C. VOLT METER BETWEEN TESTING POINTS K₁ & K₂. ADJUST VR₃ FOR ZERO 2.01 VOLTS.
- THE LOAD RESISTOR SHOULD BE ABLE TO DISSIPATE THE FULL POWER OF THE AMPLIFIER, OR 330 WATTS.

NOTES

- ALL RESISTORS 1/2W 10% UNLESS OTHERWISE SPECIFIED.
- ALL CAPACITORS IN MFD & 400V UNLESS OTHERWISE SPECIFIED.
- D.C. VOLTAGE READINGS WITH NO SIGNAL APPLIED USING A 20,000 Ω PER VOLT METER.
- NUMBERS IN PARENTHESIS REFER TO AMPEG'S PART NO.
- WHEN P₂ CONNECTOR (PREAMPLIFIER) IS DISCONNECTED VOLTAGE AT POINT "B" WILL RISE TO 600 VOLTS.
- CIRCUIT OF CHASSIS MAY VARY SLIGHTLY FROM THAT SHOWN HERE DUE TO NORMAL PRODUCTION CHANGES.

MODEL SVT POWER AMPLIFIER (6550 - OUTPUT TUBES)

ampeg

DRN. BY	DATE	DWG. NO.	PART NO.	REV.
S. C.	1070	05800	400255	