

AE-252 Final Exam Requirements

1. Draw JFET schematic from memory.

- Label input, output and power connections.

2. Draw block diagram from xlr input to xlr output. The hit list should include:

- **Phantom power distribution**
 - Switch
 - LED and its resistor
 - RC slow ramp-up time constant
(Google RC time constant and provide a summary)
 - 6k8 resistors
- **Input pad** (primary side of Input xfrmr)
 - Three position switch plus trim pot
- **Output Load** (secondary side of Input xfrmr)
 - **Include** Flat, Air and Warm positions
- **Direct Input "Hi-Z" Jack**
 - With Normal
 - Feed to and include Input Sensitivity Pot
- **First Stage Preamp** (triangle)
 - Feed to and include Output Level Pot
- **Second Stage Preamp (triangle)**
 - Feed to primary side of output transformer
 - Optional unbalanced output on 1/4-inch connector
 - Wire Output xfrmr secondary to XLR output (male) connector
 - Include polarity reverse and optional Load / Termination switches
 - Optional balanced output on 1/4-inch connector