

## AE-252 Mid-term exam

NAME: \_\_\_\_\_ DATE: \_\_\_\_\_

The questions in this exam relate specifically to the **power supply schematic** for the *Vari-fi* JFET preamp kit, which can be downloaded at:

[http://www.tangible-technology.com/ipr/AE230/html/wk\\_4/vari-fi/jfet\\_pre/VF\\_psu.html](http://www.tangible-technology.com/ipr/AE230/html/wk_4/vari-fi/jfet_pre/VF_psu.html)

PLEASE do the work in pencil, show all work (starting with formulae) and be sure to specify quantity (volts, amps, watts, dB, etc.).

1. The *Vari-fi* JFET mic preamplifier consists of two gain stages each on its own printed circuit board (PCB). Each PCB requires 24-volts dc @ 40mA from the power supply unit (PSU). The equivalent resistance of each preamp board is \_\_\_\_\_-ohms and each dissipates \_\_\_\_\_ watts.

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2. The low-voltage secondary of the power transformer (xfrmr) connects to the PSU via connector \_\_\_\_\_. The secondary has two \_\_\_\_-volt windings that have a common connection at J-1 pins \_\_\_\_ and \_\_\_\_\_. This common connection becomes \_\_\_\_\_ and travels, from left to right, to pin-\_\_\_\_\_ of J-2.
  3. Power for the preamps starts at the transformer secondary and is rectified by diodes \_\_\_\_\_ and \_\_\_\_\_, a \_\_\_\_\_-wave rectifier, after which the raw DC is filtered by \_\_\_\_\_.
  4. This is followed by a voltage divider that consists of \_\_\_\_\_ and \_\_\_\_\_.
  5. The voltage drop across R-1 is \_\_\_\_\_

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6. The current through R-1 is \_\_\_\_\_

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7. The power dissipated by R-1 is \_\_\_\_\_

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8. The purpose of Z-1 is \_\_\_\_\_.

9. The base-emitter voltage drop for Q-1 should be \_\_\_\_\_.

10. The measured base-emitter voltage is \_\_\_\_\_.

11. The preamp PCBs require approximately 24-volts and the PSU delivers within \_\_\_\_\_ per cent.

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12. As mentioned in question-1, when testing the PSU, two resistors simulated the load of each preamp module. These resistors were connected between J2 pin-3 and \_\_\_\_\_ (J-\_\_\_\_, pin-\_\_\_\_\_). In the schematic, the power supply output voltage is slightly higher and the closest standard resistor values (that were near at hand) were 620-Ω @ 1 watt each. What is the current through each resistor and the power dissipation?

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13. For all device ratings - fuses (current), Resistors (wattage), Capacitors (voltage) - here is the formula to correlate the safe operating range so that it is within 75% of the devices rated value.

$\frac{75}{100} = \frac{x}{y}$ <p style="text-align: center;"> <small>x (the current, voltage or wattage at the device)</small>  <small>y (the device's rating)</small> </p>	<p>For example, a 20amp circuit breaker should see no more than 15 amps of continuous current. By using the formula and cross-multiplying (<math>100x = 75*y = 100x = 75*20</math>) you can see that (<math>x = .75 * 20A = 15A</math>) or <math>x = 1500/100 = 15A</math>. Conversely, if you know the current, voltage or wattage at the device, you can then calculate the safe rating <b>above</b> which the device should be rated.</p>
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Based on this formula, **calculate the safe power rating for the resistors in question-12** to the nearest rounded-up wattage (typical wattage values are 1/4, 1/2, 1, 2, 3, 5, 10).

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14. The phantom power supply is fed from one leg of the xfmr secondary through \_\_\_\_\_ and \_\_\_\_\_ before reaching the rectifier, which consists of \_\_\_\_\_ and \_\_\_\_\_. This circuit is essentially two \_\_\_\_\_-wave rectifiers in a \_\_\_\_\_ configuration, named because it produces twice as much DC voltage than the RMS AC voltage feeding it.

15. Once rectified, the raw DC is filtered by \_\_\_\_\_ and clamped at approximately \_\_\_\_\_ volts by the two series \_\_\_\_\_, designated \_\_\_\_\_ and \_\_\_\_\_.

16. The phantom power output appears at pin-\_\_\_\_\_ of J-\_\_\_\_\_

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17. The phantom power supply also fed a 3k4 resistive load to simulate the worst case scenario (shorting the the two \_\_\_\_\_-Ω phantom resistors at pin-2 and pin-3 of the XLR to ground).

18. As stated in note-3 on the schematic, the no-load to worst case full-load output of the power supply ranges from 51.3 volts to 45.6 volts. This change in load is reflected back at R4. What is the AC current through, and the power dissipated by R4 from no-load to full-load?

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19. Is the wattage dissipated by R4 within 75% of the resistor's rating on the schematic? (show work)

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20. The power indicating LED is designated \_\_\_\_\_. Is it before or after the voltage regulator? \_\_\_\_\_

21. The resistor, R- \_\_\_\_\_, feeding the LED has to "throw away" \_\_\_\_\_ volts ( $V_{R3}$ ) in order to not blow up the LED. What is the current through R-4 and DL-1?

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22. Based on all the information available to you, what is the voltage at DL-1?

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$I=E/R$        $E=IR$        $R=E/I$        $P=VI$        $P=I^2R$        $P=E^2/R$